



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Warren M. Farnworth

**Serial No.:** 09/548,942

**Filed:** April 13, 2000

**For:** METHOD AND APPARATUS OF  
INTERPOSING VOLTAGE REFERENCE  
TRACES BETWEEN SIGNAL TRACES IN  
SEMICONDUCTOR DEVICES

**Confirmation No.:** 6934

**Examiner:** B. Lee

**Group Art Unit:** 2817

**Attorney Docket No.:** 2269-4161US  
(98-1265.00/US)

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**AMENDMENT ACCOMPANYING RCE**

Mail Stop RCE  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

The following amendments and remarks are filed in response to the Examiner's remarks in the Final Office Action mailed July 25, 2005 and the Advisory Action mailed October 12, 2005.

**Amendments to the Specification** begin on page 3 of this paper.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 5 of this paper.

**Remarks regarding the Final Office Action** begin on page 9 of this paper.

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**Remarks regarding the Advisory Action** begin on page 14 of this paper.